Fig1

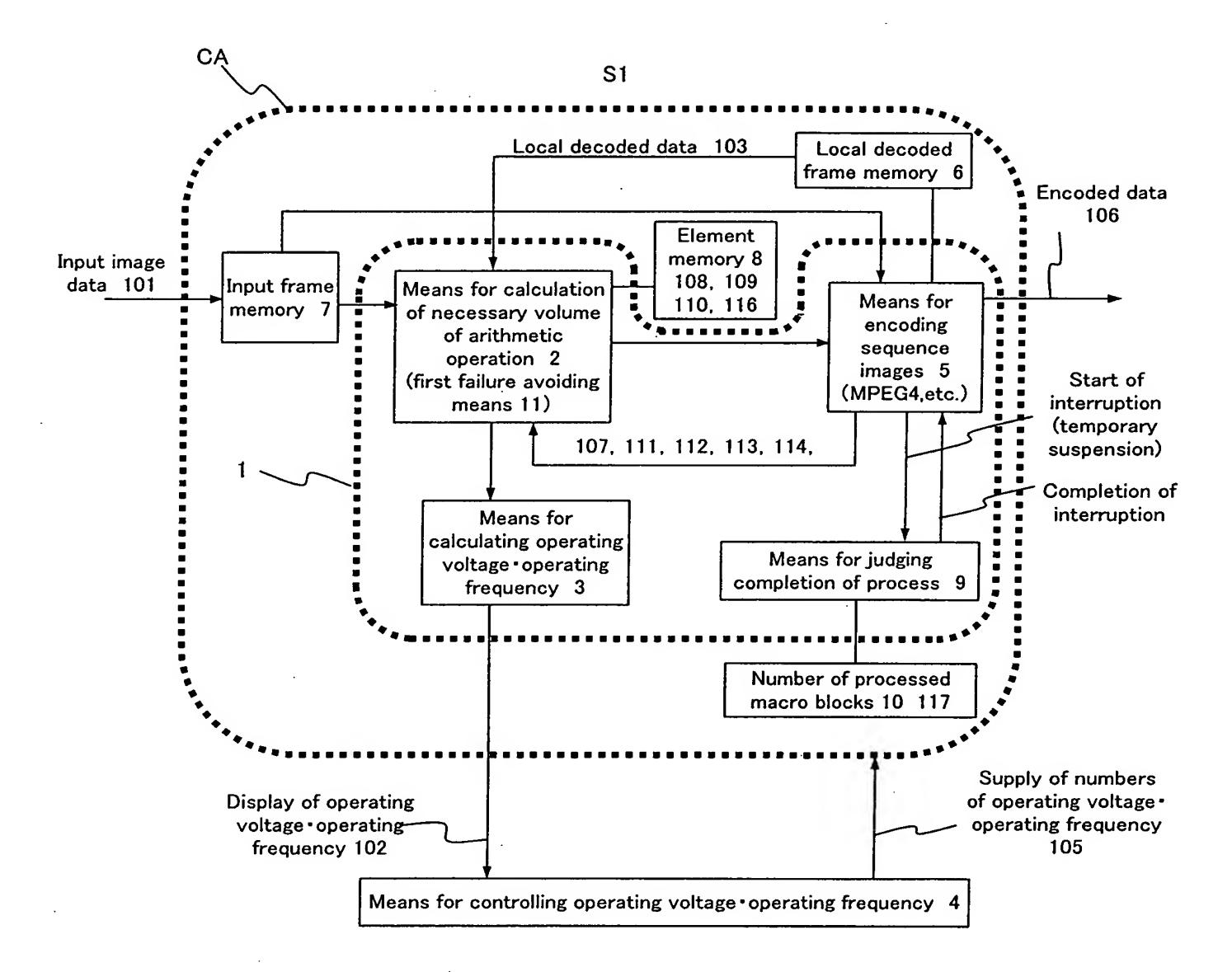


Fig2

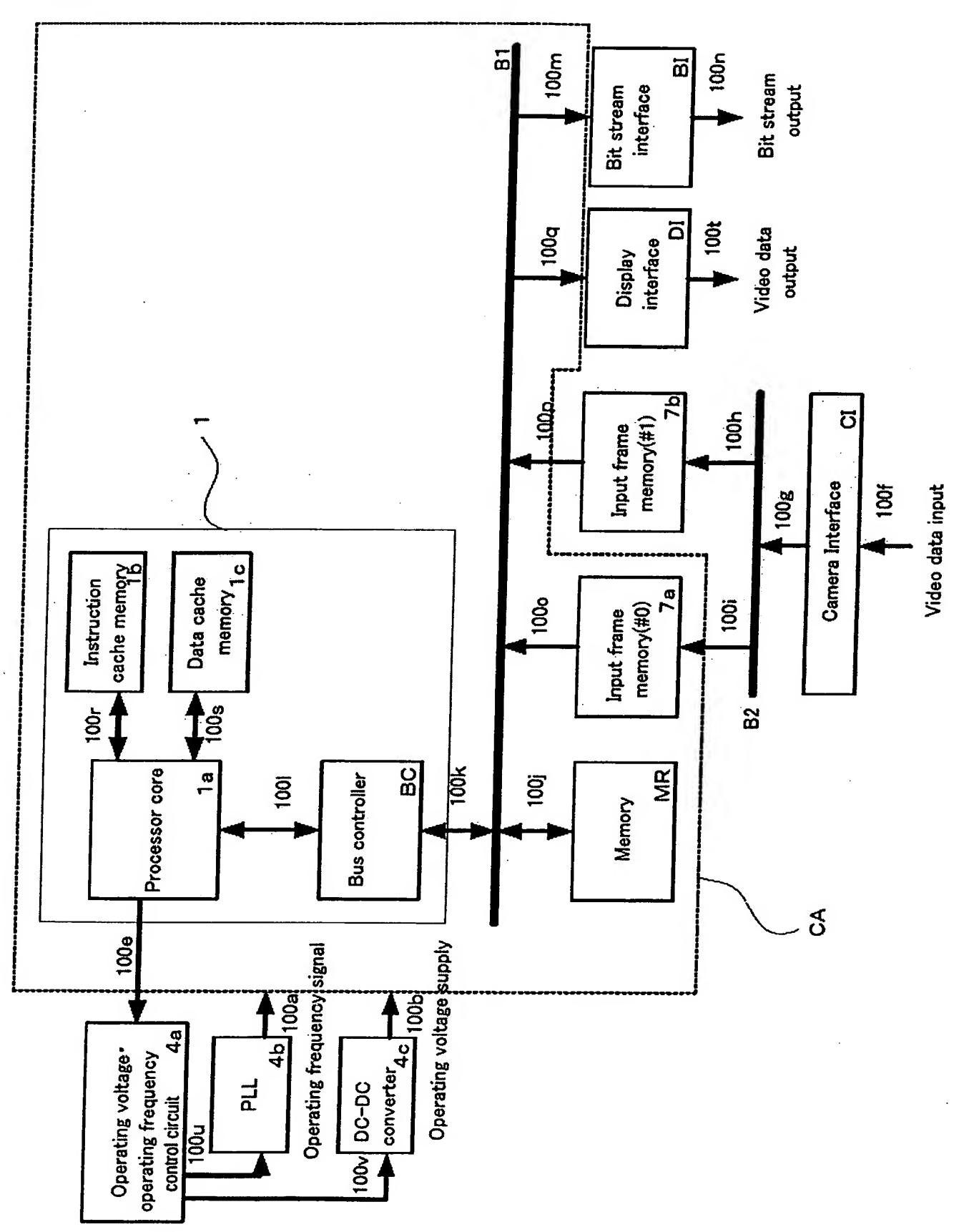


Fig3

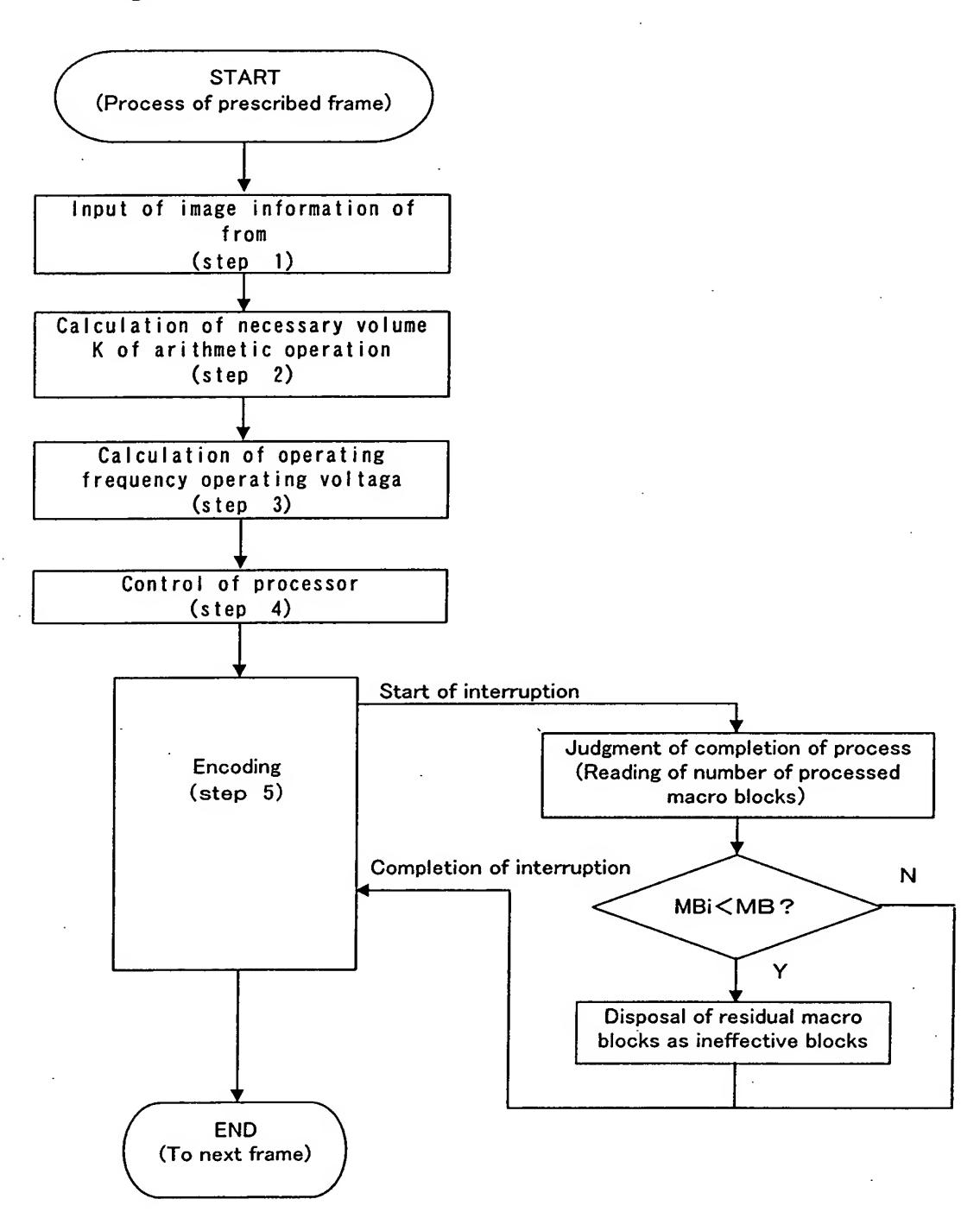


Fig4

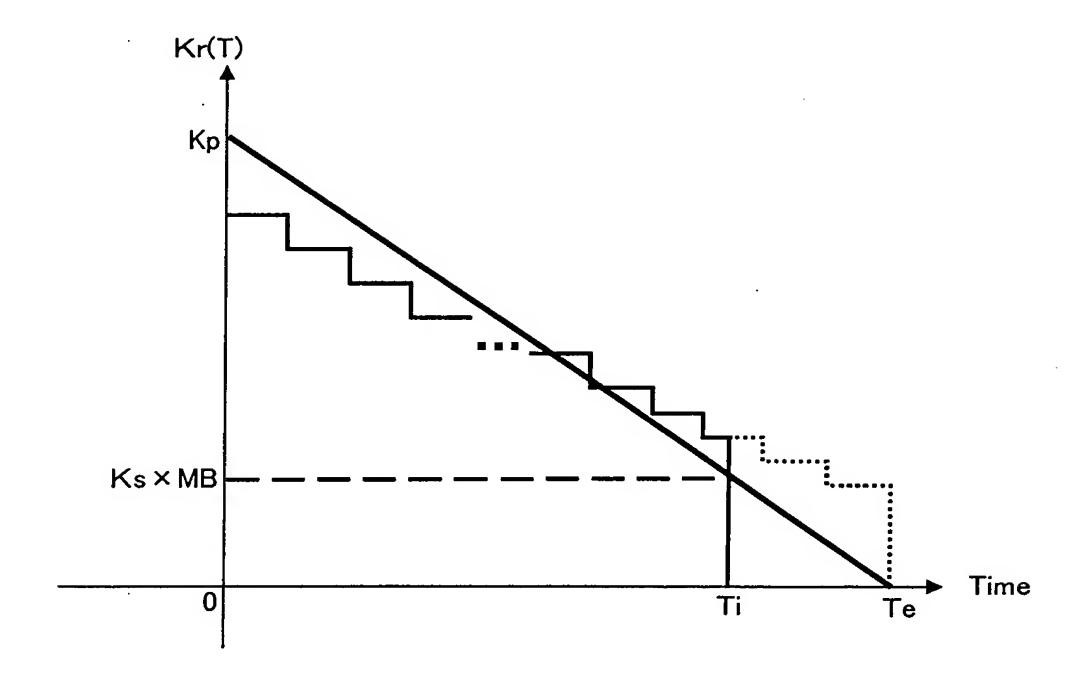
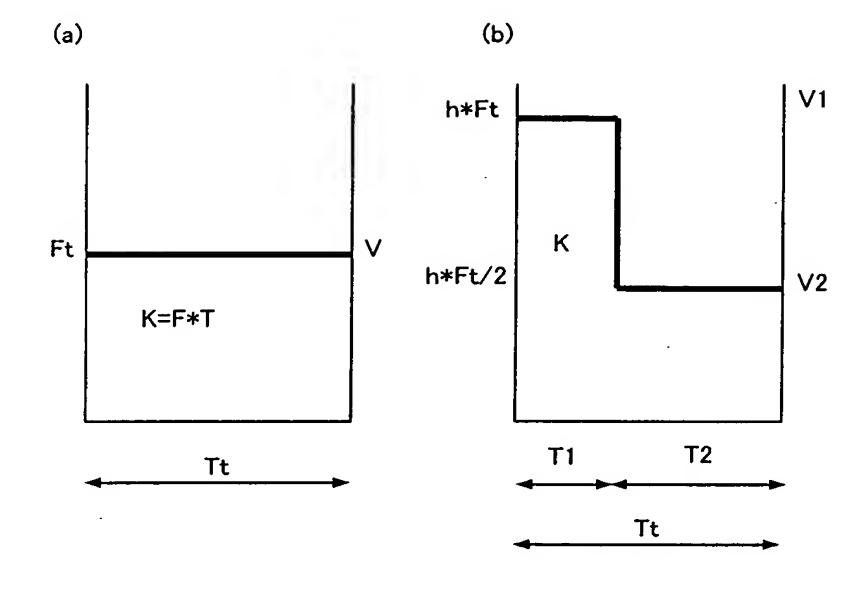


Fig5

Operating frequency (cycle/second)	Operating voltage (V)
f (1)	V (1)
f (2)	V (2)
f (3)	V (3)
•	•
f (r)	V (r)

When t>s(t) and s denotes natural numbers of not more than r) f(t)>f(s) V(t)>f(s)

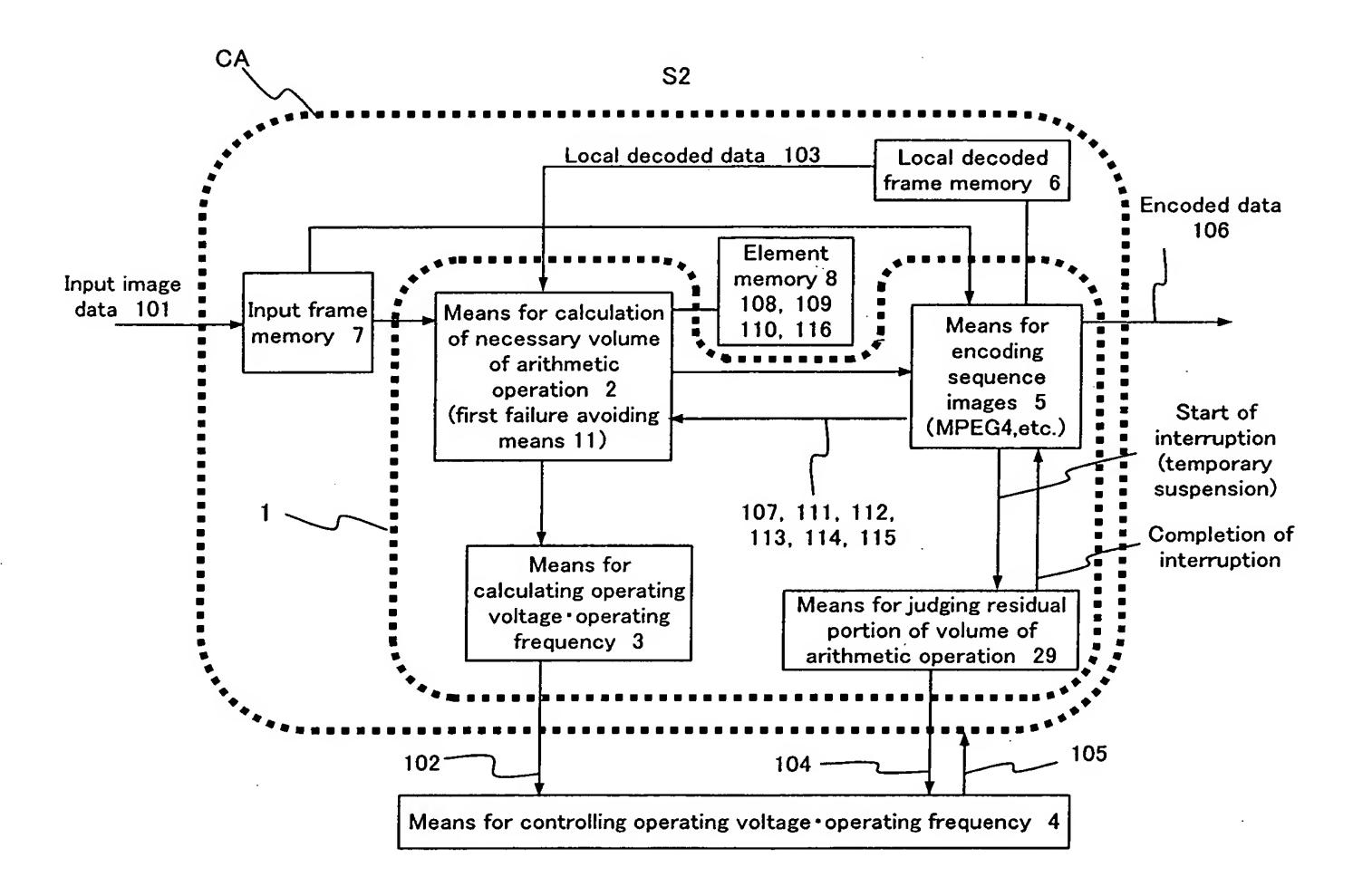
Fig6



Case 1

Case 2

Fig7



•:

Fig8

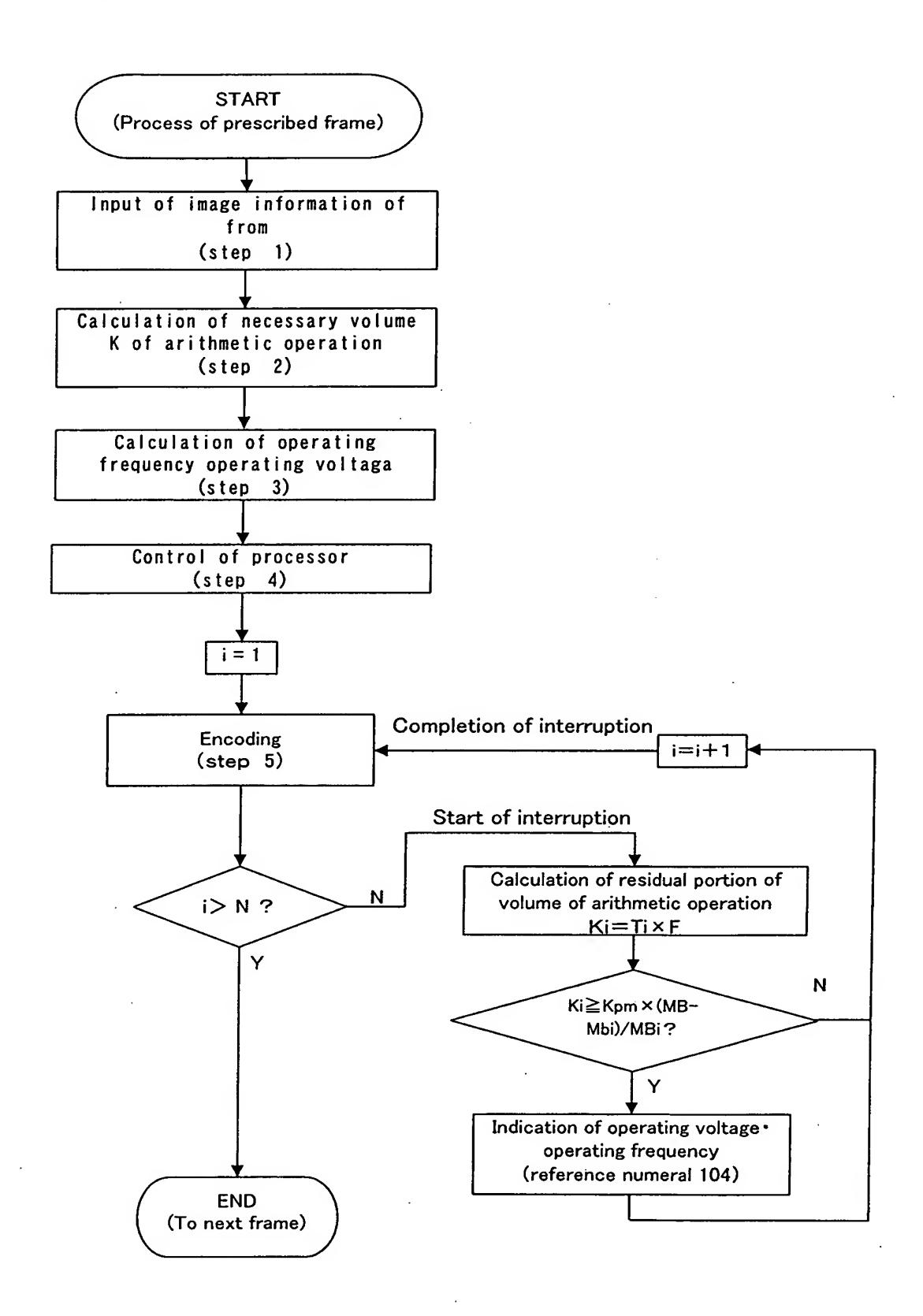
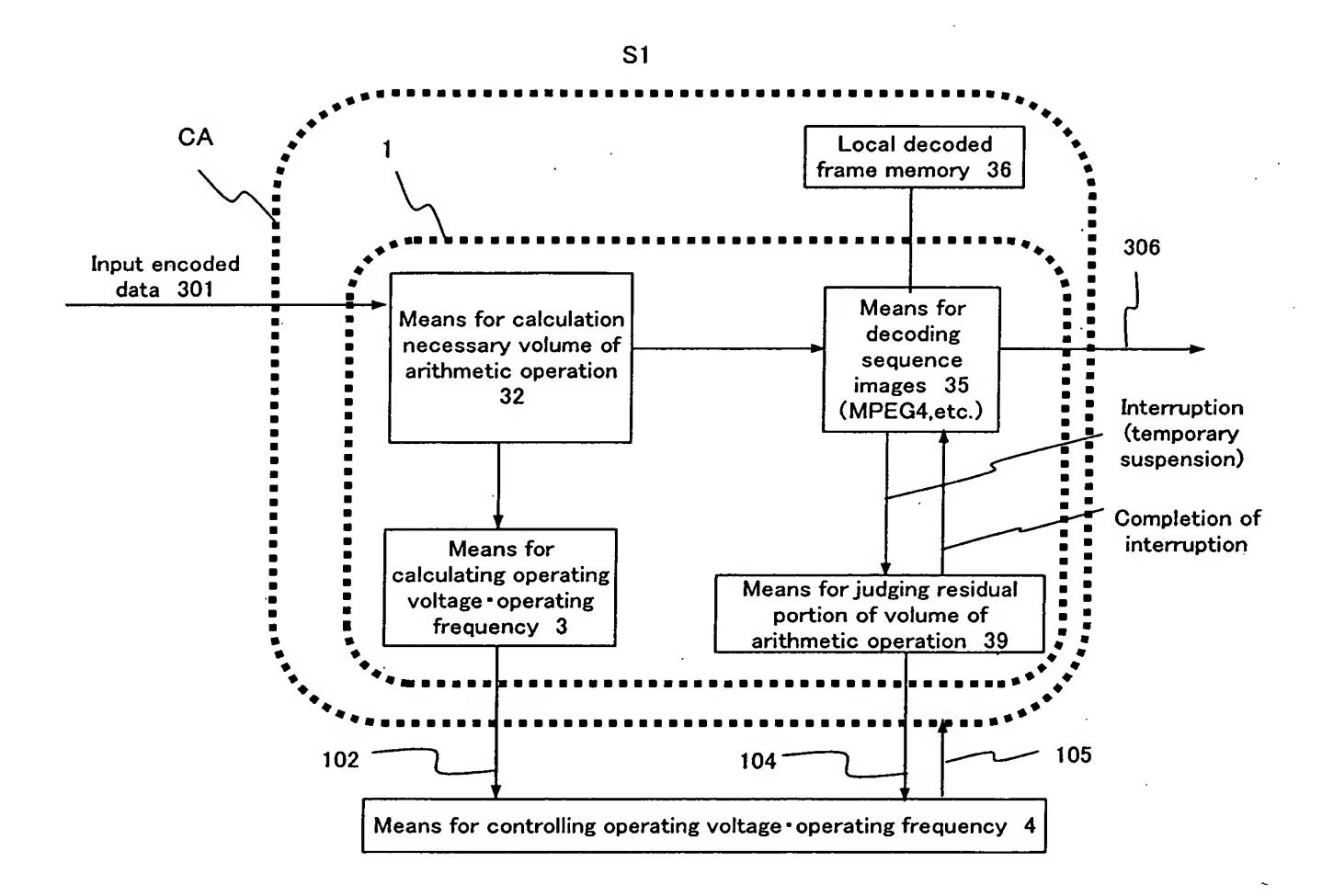


Fig9





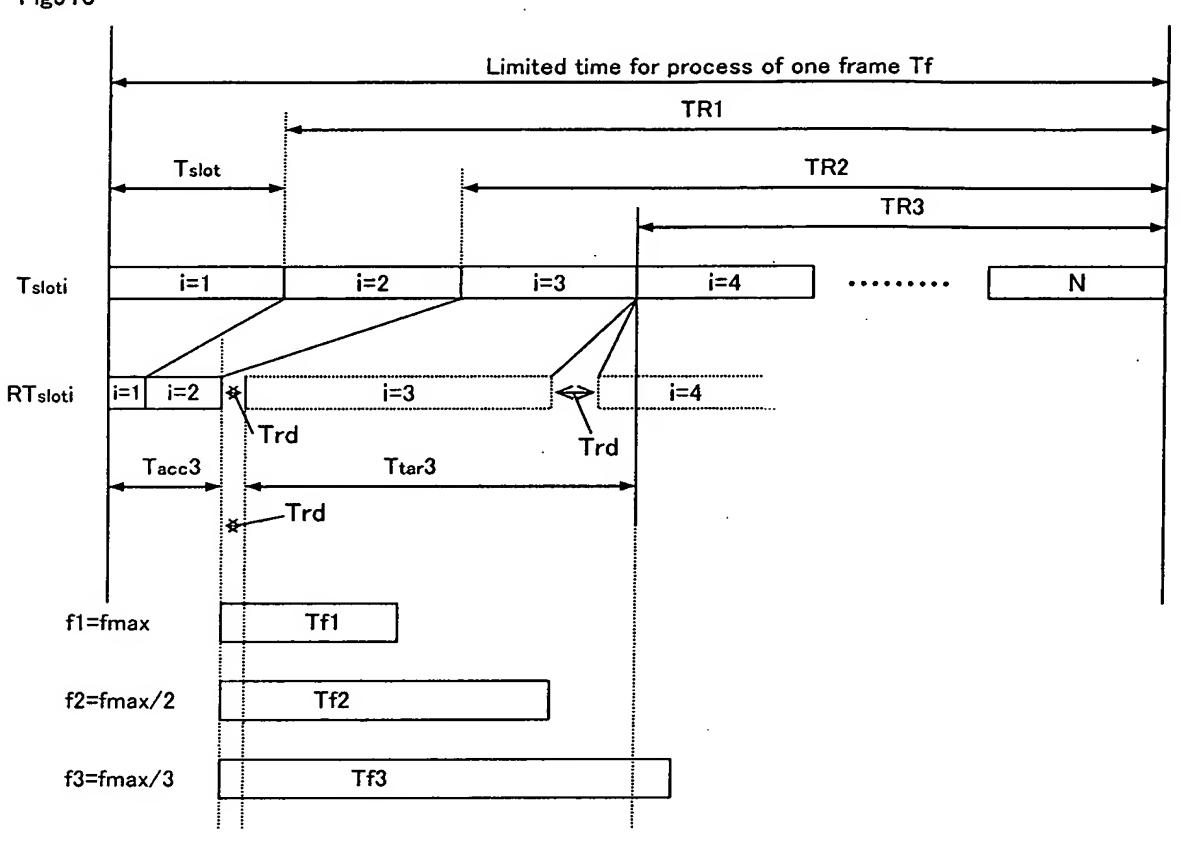
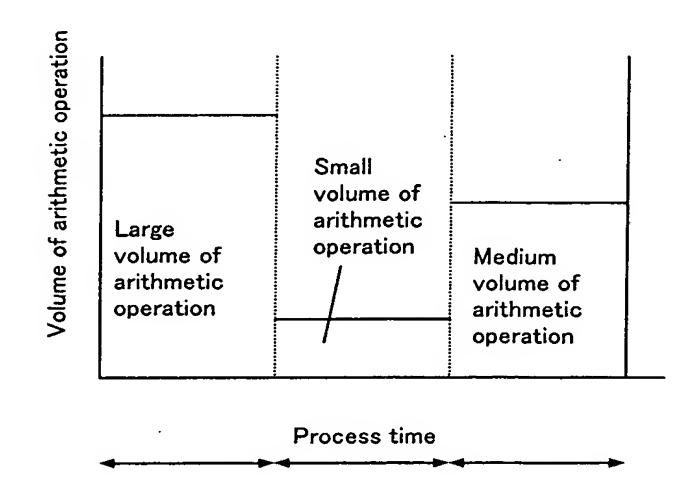


Fig11



Operating frequency f2 is selected for time slot 3